



## INFORMATION DISCLOSURE CITATION

(Use several sheets if necessary)

PTO Form 1449

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Attorney Docket No.  
040894-7183

Application No.: 10/525,045

Applicants: Shigeki SAKAI

Filing Date: February 18, 2005

Confirmation No.: 5664

Group Art Unit: 2813

## U.S. PATENT DOCUMENTS

| *Examiner<br>Initial | Document<br>Number | Date           | Name       | Class | Sub<br>Class | Filing Date |
|----------------------|--------------------|----------------|------------|-------|--------------|-------------|
| MDH                  | 6,207,589 B1       | March 27, 2001 | Ma et al.  | 438   | 785          |             |
| MDH                  | 6,080,593          | June 27, 2000  | Kim et al. | 438   | 3            |             |
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## FOREIGN PATENT DOCUMENTS

|  | Document<br>Number | Date | Country | Class | Sub<br>Class | Translation |    |
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## OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

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Examiner

*Unwani D. Harris*

Date Considered

*3/1/07*

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| <b>INFORMATION DISCLOSURE CITATION</b><br>(Use several sheets if necessary)<br><br>PTO Form 1449 |  | Attorney Docket No.<br>040894-7183             | Application No.:<br>10/525,045 |
| JUN 03 2005<br>U.S. PATENT & TRADEMARK OFFICE                                                    |  | Applicant(s): Shigeki SAKAI<br><br>PAGE 1 of 1 |                                |
|                                                                                                  |  | Filing Date: February 18, 2005                 | Group Art Unit: 5664           |

  

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| MDH                      | JP-A-2001-291841 | Oct. 19, 2001 | JAPAN   | —     | —         | abstract              |  |
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| MDH                                                                    |  | S. Migita et al., "Epitaxial Structure $\text{SrBi}_2\text{Ta}_2\text{O}_9\langle 116 \rangle / \text{SrTiO}_3\langle 011 \rangle / \text{Ce}_{0.12}\text{Zr}_{0.88}\langle 001 \rangle / \text{Si}\langle 001 \rangle$ for Ferroelectric-Gate FET Memory," <i>Integrated Ferroelectrics</i> , Vol. 40, pp. 2135-143 (2001). |
| ↓                                                                      |  | Shinji Migita et al., "Fabrication and Electrical Properties of Ferroelectric-gate FET with Epitaxial Gate Structure," <i>Denshi Jōhō Tanshin Gakkai Ronbun-shi</i> , Vol. J85-C, No. 1 (January 2002 issue), pp. 14-22.                                                                                                     |
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| Examiner: <i>Gregory D. Harrison</i> | Date Considered: <i>8/31/06</i> |
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